

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,873,383 B1
APPLICATION NO. : 09/445523
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INVENTOR(S) : Tsuyoshi Maeda et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col 7, Line 1	First "diode" should be --transistors--
Col. 11, Line 21	Delete "a"
Col. 12, Line 20	"th e" should be --the--
Col. 12, Line 35	"T his" should be --This--
Col. 13, Line 15	Before "coupling" insert --capacitive--
Col. 15, Line 16	"It" should be --If--
Col. 21, Line 65	"40S" should be --405--
Col. 23, Line 48	"eight" should be --eighth--
Col. 24, Line 23	"842" should be --841--
Col. 24, Line 24	"842" should be --843--
Col. 24, Line 32	"eight" should be --eighth--
Col. 24, Line 49	After "is" insert --a--
Col. 25, Line 19	After "according" insert --to--
Col. 26, Line 28	"minitor" should be --monitor--
Col. 26, Lines 66-67	Delete "reflectors further comprise insular"
Col. 27, Line 11	After "formed" insert --directly--
Col. 27, Line 37	"transflector" should be --transflectors--
Col. 27, Line 40	After "formed" insert --directly--
Col. 27, Line 47	"transflectors" should be --reflectors--
Col. 28, Line 27	After "formed" insert --directly--
Col. 28, Line 44	After "formed" insert --directly--

Signed and Sealed this

Eighth Day of August, 2006



JON W. DUDAS
Director of the United States Patent and Trademark Office